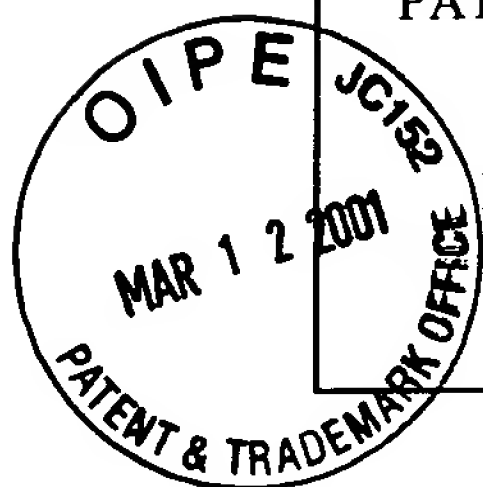


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	FILING DATE April 10, 2000	GROUP ART UNIT 2181

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EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
<i>mu</i>	5,034,964	Jul.23, 1991	Khan et al	—	—	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
<i>gm</i>	SHO 58-192154	Nov. 9, 1983	Japan	—	—	NO
<i>gm</i>	SHO 63-34795	Feb. 15, 1988	Japan	—	—	NO
<i>gm</i>	SHO 61-107453	May 26, 1986	Japan	—	—	NO
<i>gm</i>	SHO 63-91766	April 22, 1988	Japan	—	—	YES
<i>gm</i>	SHO 62-16289	Jan. 24, 1987	Japan	—	—	NO
<i>gm</i>	SHO 61-160556	Oct. 4, 1986	Japan	—	—	NO

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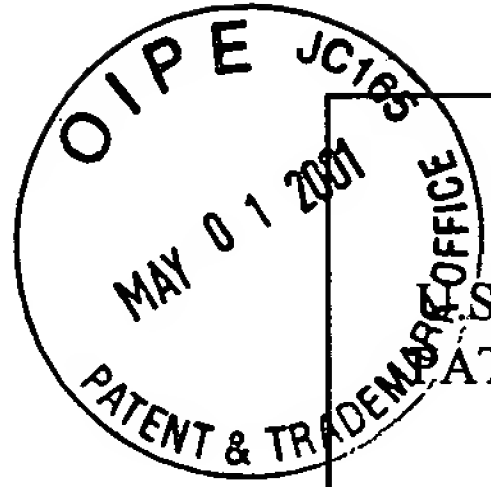
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)


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OA	4,755,937	July 5, 1989	Glier			
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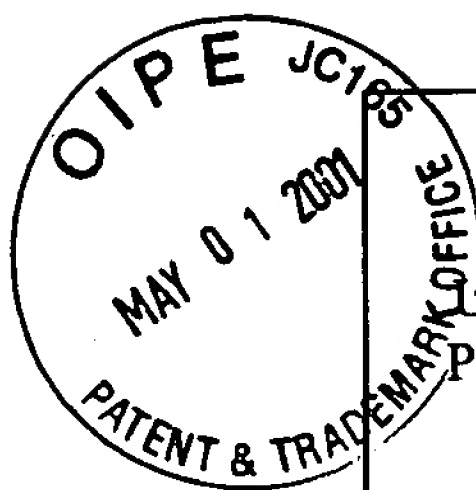
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OA	Pelgrom et al., "A 32-kbit Variable-Length Shift Register for Digital Audio Application", IEEE Journal of Solid-State Circuits, vol. sc-22, no. 3, June 1987, pp 415-422
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<i>MA</i>	5,034,964	Jul. 23, 1991	Khan et al. <i>DUR</i>	<i>—</i>	<i>—</i>	

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<i>GA</i>	Hansen et al., "A RISC MICROPROCESSOR WITH INTEGRAL MMU AND CACHE INTERFACE", MIPS Computer Systems, Sunnyvale, CA, IEEE 1986 pp 145-148
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<i>MA</i>	Moussouris, J. "The Advanced Systems Outlook-Life Beyond RISC: The next 30 years in high-performance computing", Computer Letter, July 31, 1989 (an edited excerpt from an address at the fourth annual conference on the Advanced Systems Outlook, in San Francisco, CA (June 5))

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